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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/900,940	07/09/2001	David N. Pether	00-339 1496.00116	9547

24319 7590 03/05/2004
LSI LOGIC CORPORATION
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MILPITAS, CA 95035

EXAMINER

KOSTAK, VICTOR R

ART UNIT	PAPER NUMBER
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2614

DATE MAILED: 03/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/900,940

Applicant(s)

PETHER, DAVID N.

Examiner

Victor R. Kostak

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

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1. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

2. Claims 13"b"-20 are objected to because of the following informalities:

there are two claims numbered as claim 13. The second claim 13 (13"b") and ensuing claims 14-20 have been referred to herein as claims 14-21, respectively.

Claims 12 and 13 both refer to a "said first circuit" but there is no "first circuit" recited (except in separate claim 1). Appropriate correction is required.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Bilbrey et al.

(applicant is reminded that claims 13"b" through 20 have been addressed as claims 14-21, respectively).

In his multiple effect video processing system, Bilbrey presents comprehensive disclosure of hardware and functionality of the hardware components used to alter resolution, for example (by moasicking, for example: col. 47 line 32+). The arrangement of blocks 20, 30 and 40 can be considered a first (composite) circuit that generates a signal having a first resolution for eventual display on monitor 42, derived from an input signal having a second resolution

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according to control signals, and the microcomputer 50 can be considered the second circuit which generates the control signals (used to generate the first signal from the second) in response to a previous calculation by the first circuit and various input parameters (e.g. pixel intensities, color values, horizontal and vertical image dimensions, etc.), wherein the microcomputer directs the second circuit to scale and filter the input signal (e.g. col. 1 lines 21-32, filtering carried out by pixel dropping and range thresholding, for example). The controller is initially informed of the input signal parameter calculations from the first circuit in order to know what exact parameters must be used for converting it into the second signals. Alternatively, the claimed first circuit can read on the arrangement of elements 30, 40 and parts of element 20, and the second circuit (used for control signal generation and application) can read on the microcomputer 24 associated with interface controller 24, which components together carry out the selected effect processing according to programmed and adjusted control signals, thereby meeting claims 1, 12 and 13.

As for claim s 2 and 14, three-component video signals are manipulated (e.g. RGB shown as composite element 68 in Fig. 1).

As for claims 3 and 15, alpha data is also used with the three-color video data for manipulation (e.g. output element 36 in Fig. 16).

Regarding claims 4 and 16, the horizontal and vertical image dimensions are operated on separately (e.g. col. 47 line 32+).

As for claim 5, Bilbrey also can operate on pixel cells (e.g. col. 80 line 31+), which are blocks, which also means that the first circuit (or a component thereof) serves as a block move engine.

Considering claims 6 and 17, the cells (or blocks) are operated on in the effect processing (noting again col. 80 line 31+).

As for claims 7 and 18, the cells are read on a line-by-line basis (reviewing again col. 80 line 31+).

As for claims 8 and 19, the lines are process a line, write it into memory, and process the next line on a continuous basis (Bilbrey includes plural various memories including at least video buffer 56 shown in Fig. 1.

Regarding claims 9 and 20, Bilbrey discusses filtering throughout his disclosure (including both spatial and temporal), and in col. 15 spanning lines 21-68, filtering is discussed as a function applied to various effect processing (including mosaicking or scaling) for further effecting image appearance.

As for claims 10 and 21, multiple input signals can be simultaneously applied for blending, for example (e.g. col. 16 line 57+).

As for claim 11, alpha data (i.e. blending data) can be used in the scaling process since multiple effects can be applied as so preferred by the operator.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Victor R. Kostak whose telephone number is 703 305-4374. The examiner can normally be reached on Monday - Friday from 6:30am-3:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John W. Miller can be reached on 703 305-4795. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:

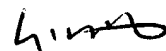
Commissioner of Patents and Trademarks
Washington, D.C. 20231

Or faxed to:

(703) 872-9306 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA., Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 308-HELP.



Victor R. Kostak
Primary Examiner
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VRK